

SFP+ 10G, 1550nm, 80KM, SINGLE MODE SNX-1512-80D

Overview:

The SFP transceivers are high performance, cost effective modules supporting data rate of 1.25Gbps and 80km transmission distance with SMF.

Data Sheet

SNX-1512-80D

The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a transimpedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement and SFF-8472 digital diagnostics functions.



- Supports up to 1.25Gbps bit rates
- Hot-pluggable SFP footprint
- 1550nm DFB laser and PIN photo detector, Up to 80km for SMF transmission
- Compliant with SFP MSA and SFF-8472 with duplex LC receptacle
- Compatible with RoHS
- Single +3.3V power supply
- Real Time Digital Diagnostic Monitoring
- Operating case temperature:

Standard: 0 to +70°C Extended: -20 to +80°C Industrial: -40 to +85°C

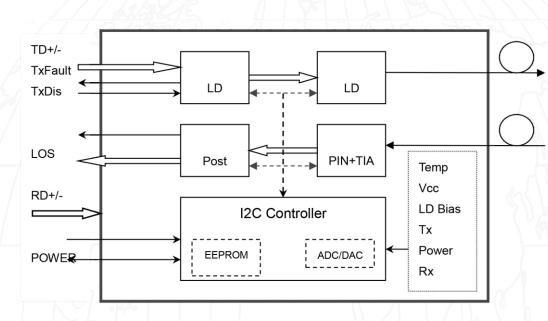
Application:

- 1.25 Gbps O ptical systems
- Gigabit Ethernet
- 1.063Gbps Fiber Channel
- Other O ptical links

SNX-1512-8







Transceiver functional diagram

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit
	Standard		0		+70	°C
Operating Case Temperature	Extended	Тс	-20		+80	°C
	Industrial		-40		+85	°c
Power Supply Voltage		Vcc	3.135	3.30	3.465	v
Power Supply Current		lcc			300	mA
Data Rate	6		0.1	1.25		Gbps

Optical and Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes
		Transmit	tter			
Centre Wavelength	λc	1530	1550	1570	nm	
Spectral Width (RMS)	Δλ			1.0	nm	
Side-Mode Suppression Ratio	SMSR	30	-	-	dB	
Average Output Power	Pout	-2		3	dBm	1
Extinction Ratio	ER	9.0			dB	
Data Input Swing Differential	V _{IN}	180		1200	mV	2



Input Differen	tial Impedance	Z _{IN}	90	100	110	Ω	
TV Disable	Disable	$V(\Lambda)$	2.0	Breed	Vcc	v	20
TX Disable	Enable		0		0.8	V	17
TV Fault	Fault		2.0		Vcc	v	$\{N, V\}$
TX Fault	Normal		0		0.8	V	$\overline{\mathcal{N}}$
			Receive	er			
Centre W	avelength	λc	1260	PR I	1610	nm	
Receiver	Sensitivity				-27	dBm	3
Receiver	Overload		-3			dBm	3
LOS De	e-Assert	LOSD	1 L		-29	dBm	
LOS	Assert	LOSA	-38	J N		dBm	
LOS Hy	steresis		0.5	11 12	4	dB	
Data Output Sv	ving Differential	V _{out}	600	800	1000	mV	4
		High	2.0	NД	Vcc	v	
L	OS	Low			0.8	v	

Notes:

- 1. The optical power is launched into SMF.
- 2. PECL input, internally AC-coupled and terminated.
- Measured with a PRBS 2^7 -1 test pattern @1250Mbps, BER $\leq 1 \times 10^{-12}$. 3.
- 4. Internally AC-coupled.

Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock		100	400	KHz
MOD_DEF (0:2)-High	V _H	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	v

Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
\sim	0 to +70		C.	5
Temperature	-20 to +80	°C	±3°C	Internal
	-40 to +85			
Voltage	3.0 to 3.6	V	±3%	Internal





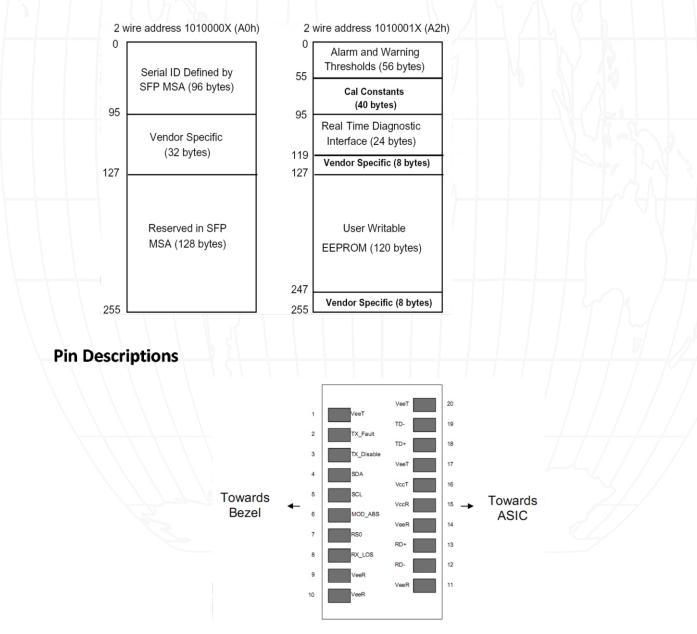
Bias Current	0 to 100	mA	±10%	Internal
TX Power	-9 to -3	dBm	±3dB	Internal
RX Power	-26 to -1	dBm	±3dB	Internal

Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

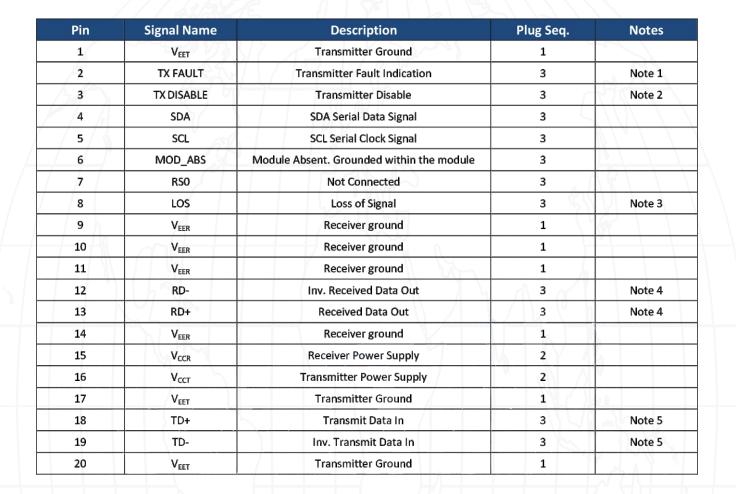
The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.





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Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

- TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- LOS is open collector output. Should be pulled up with 4.7k~10kΩ on host board to a voltage between 2.0V and 3.6V.
 Logic 0 indicates normal operation; logic 1 indicates loss of signal.
- RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 5) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

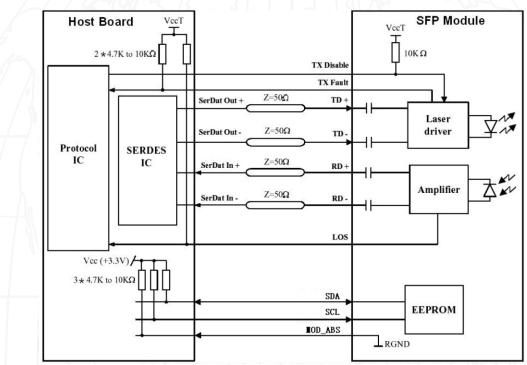
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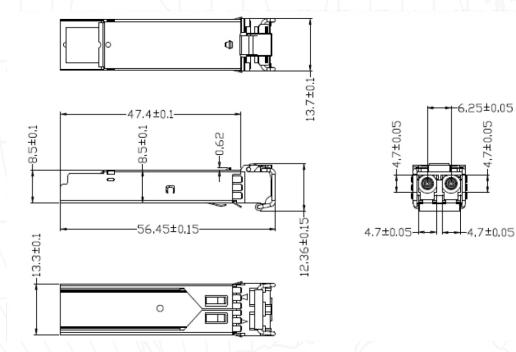
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Recommended Interface Circuit



Mechanical Dimensions



Ordering information

Part Number	Product Description
SFP-ZR	1550nm, 1.25Gbps, LC, 80km, 0°C~+70°C, with DDM